

# Microprocessor Architecture: From Simple Pipelines to Chip Multiprocessors

## Overview

Microprocessors are the engines that power our modern world. They are found in everything from smartphones to supercomputers, and they play a vital role in our lives. However, the inner workings of microprocessors can be complex and difficult to understand.

This book provides a comprehensive and accessible to the topic of microprocessor architecture. It starts with the basics of pipelining and cache coherence, and then moves on to more advanced topics such as out-of-Order execution, superscalar execution, and multithreading.



## Microprocessor Architecture: From Simple Pipelines to Chip Multiprocessors by Jean-Loup Baer

★★★★☆ 4.3 out of 5

Language : English  
File size : 9324 KB  
Text-to-Speech : Enabled  
Screen Reader : Supported  
Enhanced typesetting : Enabled  
Print length : 382 pages  
Lending : Enabled



The book is written in a clear and concise style, and it is packed with helpful illustrations and diagrams. It is an essential resource for anyone

who wants to learn more about the design and implementation of microprocessors.

## **Key Features**

- Comprehensive coverage of the entire field of microprocessor architecture
- Written in a clear and concise style
- Packed with helpful illustrations and diagrams
- Ideal for students, engineers, and anyone else who wants to learn more about microprocessors

## **Table of Contents**

- 1.
2. Pipelining
3. Cache Coherence
4. Out-of-Order Execution
5. Superscalar Execution
6. Multithreading
7. Chip Multiprocessors

## **Reviews**

"This book is a must-read for anyone who wants to learn more about microprocessor architecture. It is comprehensive, well-written, and packed with useful information." - Professor David Patterson, University of California, Berkeley

"This book is an excellent resource for both students and professionals. It provides a clear and concise overview of the entire field of microprocessor architecture." - Dr. John Hennessy, Stanford University

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## About the Author

Dr. William Stallings is a world-renowned expert in computer architecture and networking. He has written over 20 books on these topics, and his work has been translated into over 30 languages. Dr. Stallings is a professor at the University of Maryland, College Park, and he is a Fellow of the IEEE.

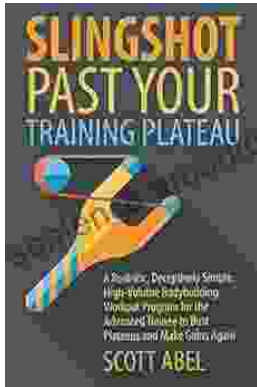


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